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THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/803,203 Confirmation No.: 5841
First Named Inventor: Bulucea, Constantin Filing Date: 17 March 2004
Group Art Unit: 2826 Examiner: Quach, T. N.
Atty. Docket No.: NS-5737 US
Title: Configuration and Fabrication of Semiconductor Structure Having
N-channel Channel-junction Field-effect Transistor
Assignee(s): National Semiconductor Corporation

Mountain View, California
3 October 2005

COMMISSIONER FOR PATENTS
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RESPONSE TO RESTRICTION REQUIREMENT

Sir:

Responsive to the Restriction Requirement mailed 2 September 2005 for the above patent application, Applicants' Attorney provisionally elects device Claims 1 - 42 in Group I for prosecution in this application.

Please telephone Applicants' Attorney at 650-964-9767 if there are any questions.

EXPRESS MAIL LABEL NO.:

EV 500 311 205 US

Respectfully submitted,

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